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| <u>Learning Outcomes</u> | The students at the end of the paper, will have some knowledge of designing a point to point optical link for a given situation. They will also be able to choose the right type of components if an assignment of optical network design is given. The course is also useful for students who would like to join telecom industries, as many aspects of practical situation are discussed during course of study. They are also taught to monitor signal losses during course of signal transmission. The student from this course will be confident | |

Course Code: ELD 202 **Title of the Course:** OPERATING SYSTEM AND RTOS

Number of Credits: 4

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| <u>Prerequisites for the course:</u> | Should have studied digital electronics at graduate level | |
| <u>Objective:</u> | This course develops to focus on concept of highlighting the various methods of improvising speed of computing machine through the operating system organization and various entity managements. Further the subject is developed to analyse the small embedded system developments through the Real Time Operating Systems for task management efficiency. | |
| <u>Content:</u> | <u>Introduction to Computer Organization and Architecture : hardware vs. software -the virtual machine concept, concept of von Neumann architecture, hardware components and functions, trends in hardware development, system configurations and classifications.</u> | 6 hours |
| | Process Description and Control: Processes, process states, processor modes, context switching, CPU scheduling algorithms, threads. | 5 hours |
| | Concurrency Control: Concurrent processes, critical section problem and solutions, mutual exclusion solution requirements, semaphores and monitors. | 5 hours |
| | Deadlocks: Characterization, detection and recovery, avoidance, prevention. | 5 hours |
| | Inter Process Communication: classical IPC problems and solutions, IPC techniques. | 3 hours |
| | The Input/Output and File Subsystem: I/O devices, controllers and channels, bus structures, I/O techniques (programmed, interrupt driven and DMA), I/O subsystem layers. Concepts of files and directories, issues and techniques for efficient storage and access of data. I/O and file system support for graphics, multimedia, databases, transaction | 6 hours |

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| | processing and networking. | |
| | The Memory Subsystem: Memory types and hierarchy, module level Organization, cache memory. Memory partitioning, swapping, paging, segmentation, virtual memory. | 8 hours |
| | The Central Processing Unit: CPU components, register sets, instruction cycles, addressing modes, instruction sets, concept of micro-programming ,Basics of RISC approach, pipelined and super-scalar approaches, vector processors and parallel processors, hardware support for the OS. | 6 hours |
| | <u>µCOS case study</u> Tutorial 1. Implementing Lower Level Shell 2. Implementing Signal in Unix 3. Hard disk partitioning in Linux | 4 hours |
| | Total | 48 |
| <u>Pedagogy:</u> | Lectures/ tutorials/assignments/self-study | |
| <u>References/Readings</u> | 1. Operating system principles, 3 rd Edition, by William Stallings – PHI (1998) 2. Operating system concepts by Silberchatz and Galvin - Addison Wesley 3. Operating system by Tanaumbua, PHI New Delhi | |
| <u>Learning Outcomes</u> | Will be able to generalize the understanding of the computing machine and various entities associated with the enhancement of the efficiency. Will be able to handle the operating system management process, memory, I/O, Secondary Disk and organizations of various. Students will be able to handle any operating system for process and task managements if follows the documentations of the same. | |

Course Code: ELC 202 **Title of the Course:** ELECTRONICS PRACTICALS-II

Number of Credits: 4

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| <u>Prerequisites for the course:</u> | Should have studied microcontrollers and embedded system. | |
| <u>Objective:</u> | The students will handle experiments on processor and | |