

Interference Cancellation in a Full duplex System

A DISSERTATION SUBMITTED TO

GOA UNIVERSITY

FOR THE DEGREE OF

MASTER OF SCIENCE

IN

ELECTRONICS

MR. UDDESH F. GAWAS, MR. HARISHCHANDRA R. MAHALE

&

MR. ROHIT M. GURAV

(M.Sc.)

DEPARTMENT OF ELECTRONICS

GOA UNIVERSITY, TALEIGAO PLATEAU - 403206,

GOA, INDIA

DATE: 31- 07- 2020

CERTIFICATE



This is to certify that the project entitled

“Interference Cancellation in a Full duplex System”

Is a bonafide work done by

Mr. Uddesh F. Gawas , Mr. Harishchandra R. Mahale, &

Mr. Rohit M. Gurav

M.Sc. Part II (Electronics)

2019-2020

The candidates have worked on the project during the period of study under my guidance and to the best of my knowledge it has not previously formed the basis of award of any previous degree or diploma at Goa University.

HOD (Electronics)

Examiner

Project Guide

ACKNOWLEDGEMENT

Many hours of hard work and sincere efforts have gone into the accomplishment of this project. All of this would not have been possible without the timely support and supervision of certain people.

Firstly, we would like to thank the most significant being in our life and that is God Himself for providing us with the mental strength, courage, resolve and motivation to complete our project.

Secondly, we express absolute sincere gratitude towards our H.O.D. Dr. Rajendra S. Gad, Dr. Gourish Naik, Dr. Jivan Parab and (PhD student) Charanarur Panem and Marlon Siqueira for their relentless encouraging and motivating attitude towards our project work from the commencement to the culmination of our project.

Also, a note of thanks to all my classmates and Ph.D. students for the much-needed assistance and support when it was desired.

Next, I would like to thank Sir William and rest of the staff of my department for handling all my requirements and would also like to extend a special thanks to Mr. Deepak Chodankar Director – Technology, at Wisdom lab, Verna, Goa. for his support and valuable time.

Lastly, a big thanks to our parents for providing us with their support and financial help. Thanks to people who we might have forgotten to mention but have been instrumental in bringing this project to a fruitful conclusion.

DECLARATION

We hereby declare that, the thesis entitled “**Interference Cancellation in a Full duplex System**” submitted by us for the award of Degree of Master of Science in the faculty of Science of Goa University, Goa – India has been completed and written by us, has not previously formed the basis for the award of any Degree or Diploma or other similar title of this or any other University in India or any other country or examining body to the best of my knowledge.

M.Sc. (Electronics)

2019-2020

Mr. Rohit M. Gurav

Mr. Harishchandra R. Mahale

Mr. Uddesh F. Gawas

Date: 31 - 07 - 2020

Place: Goa

ABSTRACT

Project Title: “SELF INTERFERENCE CANCELLATION IN A FULL DUPLEX SYSTEM”

Abstract-This paper presents the design and implementation of the In-band full duplex communication that can simultaneously transmit and receive on the same channel. The proposed scheme increases the amount of cancellable self-interference power by suppressing the distortion caused by the transmitter and receiver nonlinearities. In-band full-duplex has advantages over conventional duplexing schemes. A frequency division duplexing (FDD) system transmits and receives at the same time by using two different channels. Full duplexing increases the spectral efficiency by two times. We also propose novel air, analog and digital cancellation techniques that cancel the self-interference at receiver end, and therefore it guarantees that there is no degradation to the received signal. We have designed our simulation on air, analog and digital cancellation. Also show how our digital cancellation able to archive self-interference cancellation.

Keywords- In-band full duplex, Self-Interference Cancellation (SIC), Spectral efficiency, NLMS filter, Circulator.

TABLE OF CONTENTS

	Page
CHAPTER1: INTRODUCTION.....	1
CHAPTER 2: PROJECT OVERVIEW.....	10
CHAPTER 3: LITERATURE SURVEY.....	18
CHAPTER 4: MODULATION & MULTIPLEXING TECHNIQUES.....	25
CHAPTER 5: SELF INTERFERENCE CANCELLATION PROPOSED TECHNIQUES.....	41
CHAPTER 6: IMPLEMENTATION.....	49
CHAPTER 7: SELF INTERFERENCE CANCELLATION SIMULATION ON MATLAB SIMULINK	53
CHAPTER 8: APPLICATION IP PBX SYSTEM.....	63
CHAPTER 9: RESULT CONCLUSION & FUTURE WORK.....	77
APPENDIX – I :.....	84