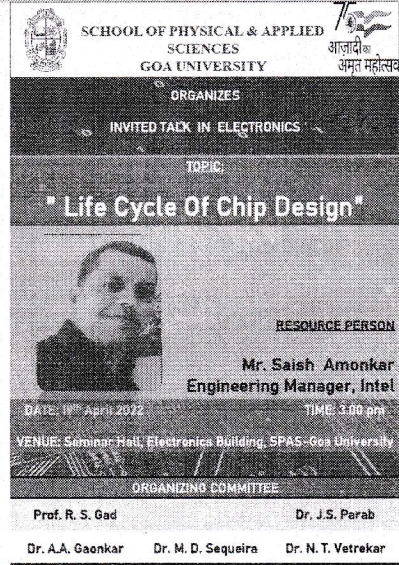

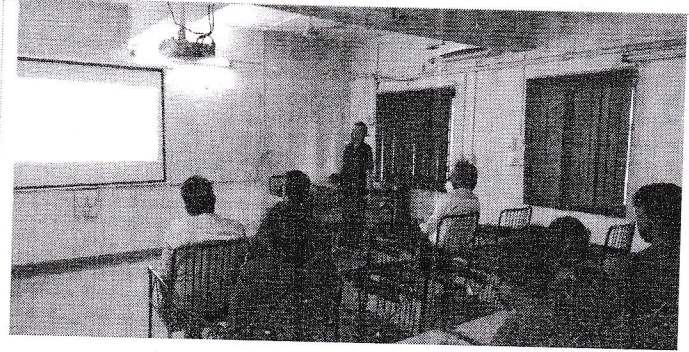
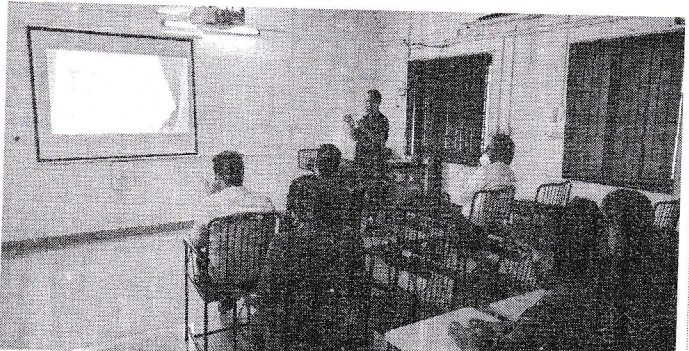




REPORT ON INVITED EXPERT TALK

Title of the event	" Life Cycle Of Chip Design"
Date and time	19 th April 2022, 3.00pm
Mode	Offline mode
Department/School/Directorate	School of Physical And Applied Sciences (Electronics)
Participants	30 participants
Resource Person	Saish Amonkar, Engineering Manager, Intel
Faculty attended	5 faculty participants
Students attended	25 student participants
No of external participants	Nil
The objectives / description of the activity (50 words)	To enhance the students knowledge on chip designing life cycle and other industrial approaches
Copy of Flyer of the event	
URL of the event	--
Photos (4)	

	  
Feedback	Good
Benefit/Key outcome of the event in terms of learning/skills/knowledge	<p>- Students had an opportunity to learn:</p> <ol style="list-style-type: none"> 1. About different stages involved in chip development. 2. Job opportunities and research scope <p>The resource person also resolved the doubts of the students.</p>


DEAN
 School of Physical and Applied Sciences
 Goa University



SCHOOL OF PHYSICAL & APPLIED
SCIENCES
GOA UNIVERSITY



ORGANIZES

INVITED TALK IN ELECTRONICS

TOPIC:

" Life Cycle Of Chip Design "



RESOURCE PERSON

Mr. Saish Amonkar
Engineering Manager, Intel

DATE: 19th April 2022

TIME: 3.00 pm

VENUE: Seminar Hall, Electronics Building, SPAS-Goa University

ORGANIZING COMMITTEE

Prof. R. S. Gad

Dr. J.S. Parab

Dr. A.A. Gaonkar

Dr. M. D. Sequeira

Dr. N. T. Vetrekar

