GU/Exam(PG)/Ph.D. Eval/Elect & Electo Eng/SDP/2022/115/64   Date: 24/01/2023

To,
Dr. Sonali Dattatraya Pandit,
Department of Electrical and Electronics Engineering,
Goa College of Engineering,
Farmagudi, Ponda, Goa.

Madam,

With reference to the thesis submitted by you for the award of the Degree of **Doctor of Philosophy in Electrical and Electronics Engineering,** thesis entitled, **"FPGA Based Controller Design for Switching Converters and Performance Analysis"**, I am pleased to inform you that you have been found qualified for the award of the said degree in accordance with the University Ordinance OA-19 on **21st December, 2022.**

The said degree will be formally conferred on you at the ensuing Convocation on your applying for the same in the prescribed form.

Yours sincerely,

(Shri. Ashok M. Chodankar)
Controller of Examinations

Copy to:
1. Dr. Vinayak Shet (Guide), Goa College of Engineering, Farmagudi, Ponda, Goa.
2. Prof. Purnanand Savoikar (Chairperson of Board of Viva-voce), Goa College of Engineering, Farmagudi, Ponda, Goa.
3. Dean, Faculty of Engineering, Goa College of Engineering, Farmagudi, Ponda, Goa.
4. HOD, Department of Electrical and Electronics Engineering, Farmagudi, Ponda, Goa.
5. The Principal, Goa College of Engineering, Farmagudi, Ponda, Goa
6. Assistant Registrar, Acad. (PG) for information.